



#### Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	$I_D$
20V	19mΩ@4.5V	5A
	26mΩ@2.5V	
-20V	45mΩ@-4.5V	-3.5A
	93mΩ@-2.5V	

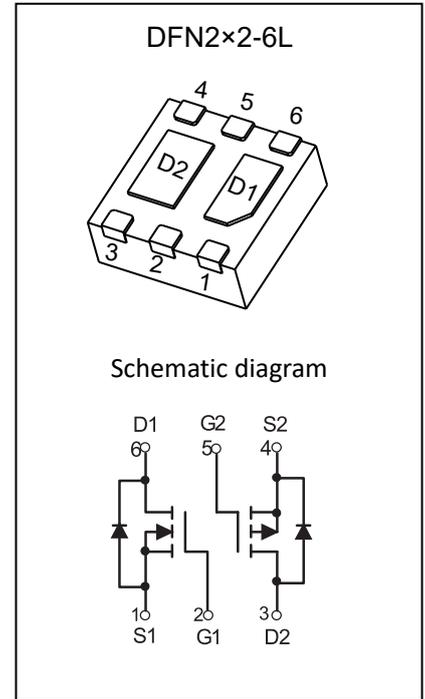
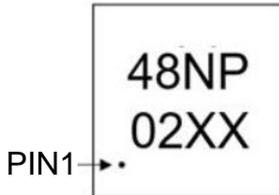
#### Feature

- Trench Technology Power MOSFET
- Low  $R_{DS(ON)}$
- Low Gate Charge

#### Application

- Load Switch
- DC/DC Converter

#### MARKING:



#### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Value	Unit
Drain - Source Voltage	$V_{DS}$	20	-20	V
Gate - Source Voltage	$V_{GS}$	$\pm 20$	$\pm 20$	V
Continuous Drain Current <sup>1,5</sup>	$I_D$	5	-3.5	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	20	-16	A
Power Dissipation <sup>4,5</sup>	$P_D$	1.5	1.3	W
Thermal Resistance from Junction to Ambient <sup>5</sup>	$R_{\theta JA}$	83	96	$^\circ\text{C}/\text{W}$
Junction Temperature	$T_J$	150	150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55~ +150	-55~ +150	$^\circ\text{C}$

## MOSFET ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

### NMOS:

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
<b>Off Characteristics</b>						
Drain - Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	20			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 20V, V_{GS} = 0V$			1	$\mu A$
Gate - Body Leakage Current	$I_{GSS}$	$V_{GS} = \pm 10V, V_{DS} = 0V$			$\pm 100$	nA
<b>On Characteristics<sup>3</sup></b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	0.4	0.8	1.0	V
Drain-source On-resistance	$R_{DS(on)}$	$V_{GS} = 4.5V, I_D = 5A$		19	27	m $\Omega$
		$V_{GS} = 2.5V, I_D = 5A$		26	36	
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 10V, V_{GS} = 0V, f = 1MHz$		247		pF
Output Capacitance	$C_{oss}$			47		
Reverse Transfer Capacitance	$C_{rss}$			42		
Gate Resistance	$R_g$	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$		1.6		$\Omega$
<b>Switching Characteristics</b>						
Total Gate Charge	$Q_g$	$V_{DS} = 10V, V_{GS} = 10V, I_D = 3.1A$		3.6		nC
Gate-source Charge	$Q_{gs}$			0.7		
Gate-drain Charge	$Q_{gd}$			1.2		
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 10V, V_{GS} = 4.5V,$ $R_L = 3.3\Omega, R_G = 6\Omega$		2.6		ns
Turn-on Rise Time	$t_r$			3.3		
Turn-off Delay Time	$t_{d(off)}$			22		
Turn-off Fall Time	$t_f$			4		
<b>Source - Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>3</sup>	$V_{SD}$	$V_{GS} = 0V, I_S = 0.94A$			1.2	V

## MOSFET ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

### PMOS:

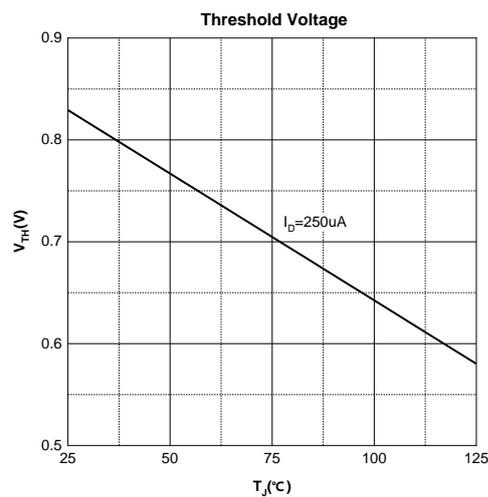
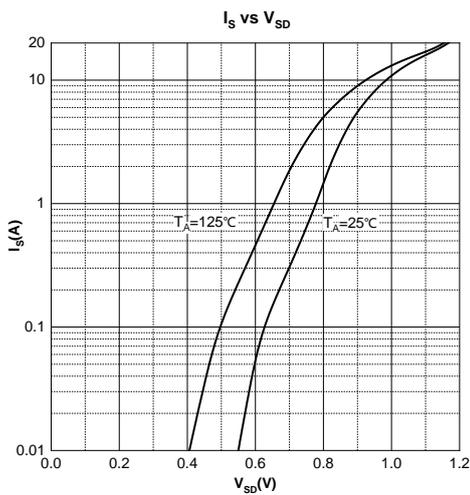
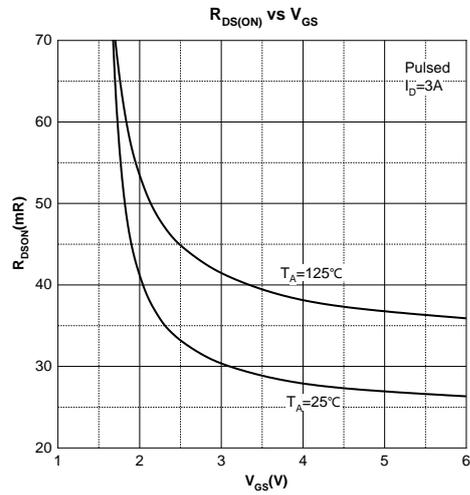
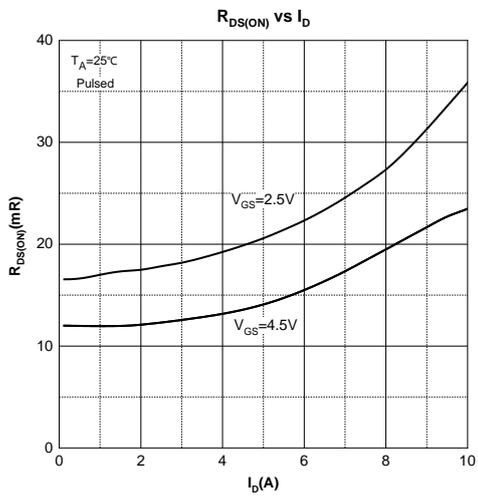
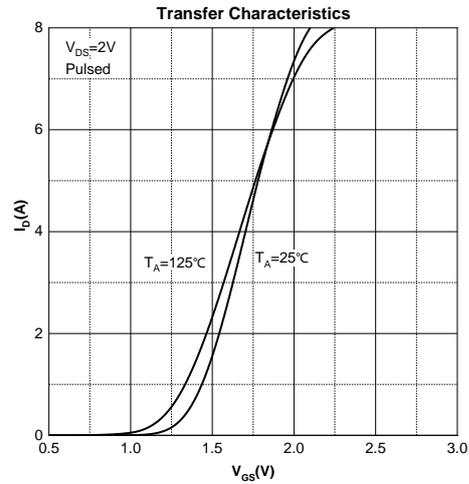
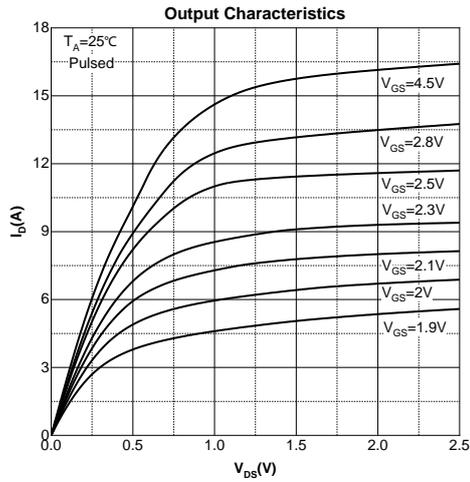
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
<b>Off Characteristics</b>						
Drain - Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-20			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -20V, V_{GS} = 0V$			-1	$\mu A$
Gate - Body Leakage Current	$I_{GSS}$	$V_{GS} = \pm 10V, V_{DS} = 0V$			$\pm 100$	$\mu A$
<b>On Characteristics<sup>3</sup></b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-0.4	-0.8	-1.0	V
Drain-source On-resistance	$R_{DS(on)}$	$V_{GS} = -4.5V, I_D = -3A$		48	63	m $\Omega$
		$V_{GS} = -2.5V, I_D = -3A$		65	93	
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS} = -10V, V_{GS} = 0V, f = 1MHz$		355		pF
Output Capacitance	$C_{oss}$			57		
Reverse Transfer Capacitance	$C_{rss}$			49		
Gate Resistance	$R_g$	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$		5.7		$\Omega$
<b>Switching Characteristics</b>						
Total Gate Charge	$Q_g$	$V_{DS} = -10V, V_{GS} = -10V, I_D = -5.0A$		4.6		nC
Gate-source Charge	$Q_{gs}$			0.9		
Gate-drain Charge	$Q_{gd}$			1.3		
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = -10V, V_{GS} = -4.5V, I_D = -1A, R_L = 10\Omega, R_G = 1\Omega$		9		ns
Turn-on Rise Time	$t_r$			33		
Turn-off Delay Time	$t_{d(off)}$			23		
Turn-off Fall Time	$t_f$			9		
<b>Source - Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>3</sup>	$V_{SD}$	$V_{GS} = 0V, I_S = -1A$			-1.2	V

### Notes :

- 1.The maximum current rating is limited by package.
- 2.Pulse Test : Pulse Width  $\leq 10\mu s$ , duty cycle  $\leq 1\%$ .
- 3.Pulse Test : Pulse Width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ .
- 4.The power dissipation  $P_D$  is limited by  $T_{J(MAX)} = 150^\circ\text{C}$ .
- 5.Device mounted on  $1in^2$  FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$ .

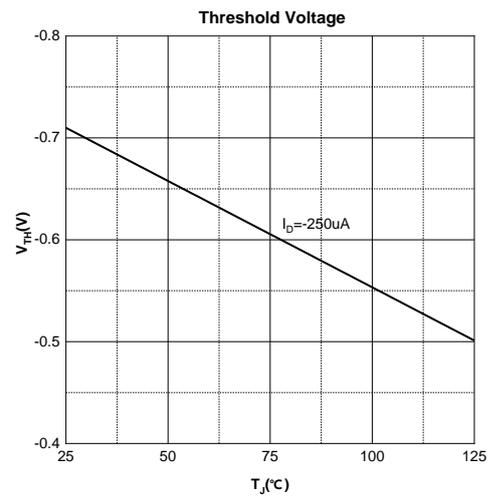
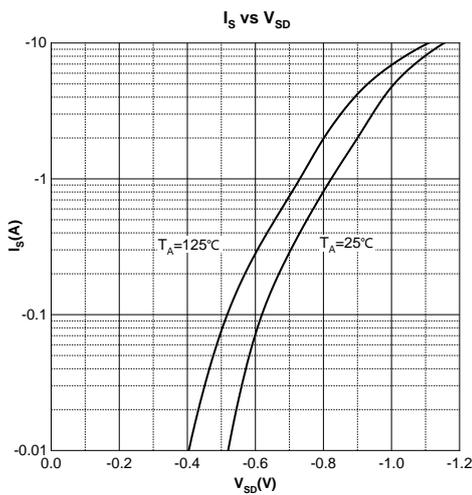
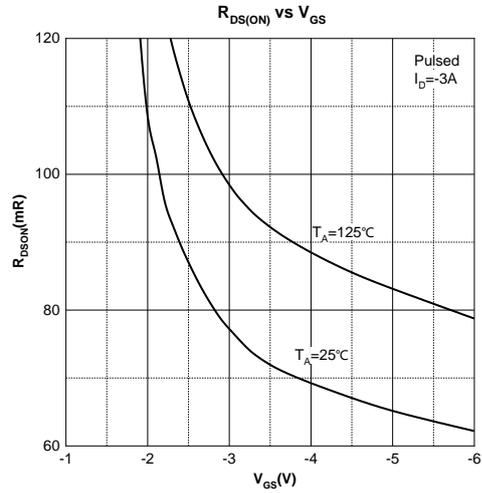
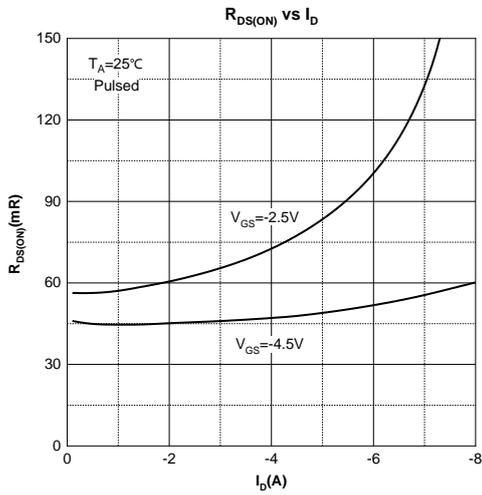
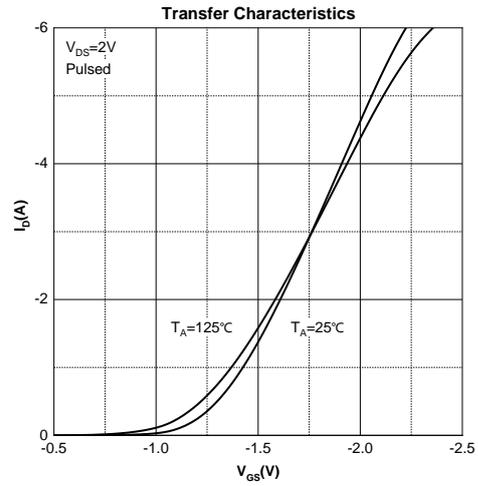
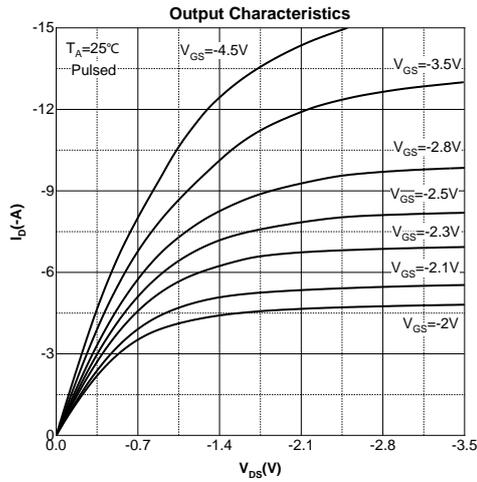
## Typical Characteristics

NMOS:

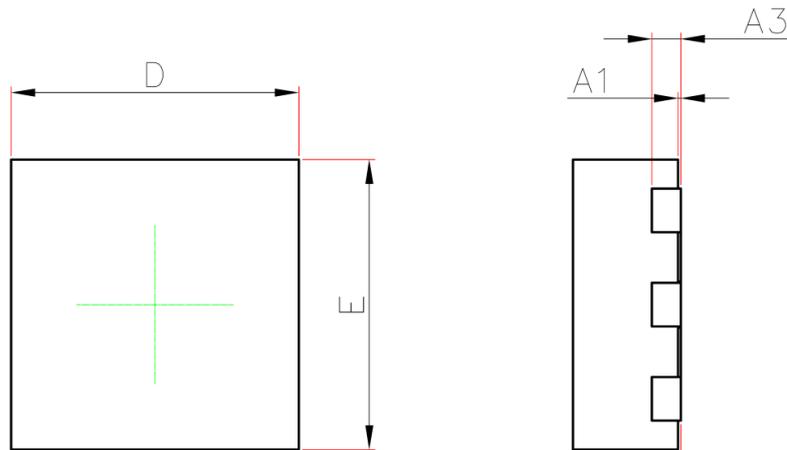


**Typical Characteristics**

**PMOS:**

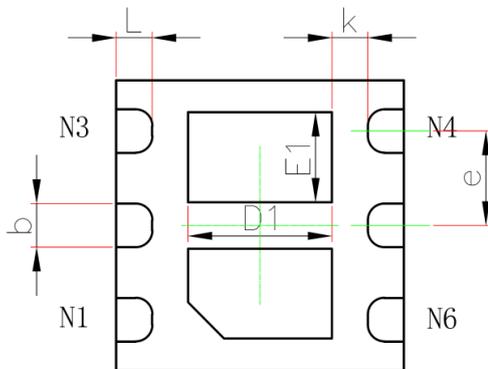


## DFN2×2-6L Package Information



TOP VIEW

SIDE VIEW



BOTTOM VIEW

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.800	0.028	0.031
A1	0	0.050	0	0.002
A3	2.03REF		0.008REF	
D	1.900	2.100	0.075	0.083
E	1.900	2.100	0.075	0.083
D1	0.900	1.100	0.035	0.043
E1	0.520	0.720	0.020	0.028
k	0.200MIN		0.008MIN	
b	0.250	0.350	0.010	0.014
e	0.65BSC		0.026TYP	
L	0.174	0.326	0.007	0.013