



GP
ELECTRONICS

GPT024N10NTH
100V N-Channel MOSFET

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)}TYP$	I_D
100V	2.7mΩ@10V	257A

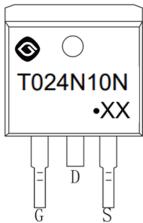
Feature

- Split Gate Trench Technology
- Low $R_{DS(ON)}$
- Low Gate Charge
- Low Gate Resistance
- 100% UIS Tested

Application

- Power Switching Application

MARKING:

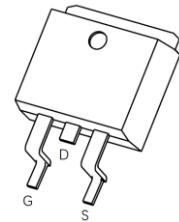


T024N10N = Device Code

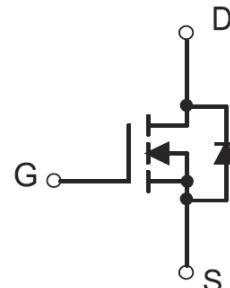
XX = Date Code

Solid Dot = Green Indicator

TO-263-2L



Schematic diagram



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

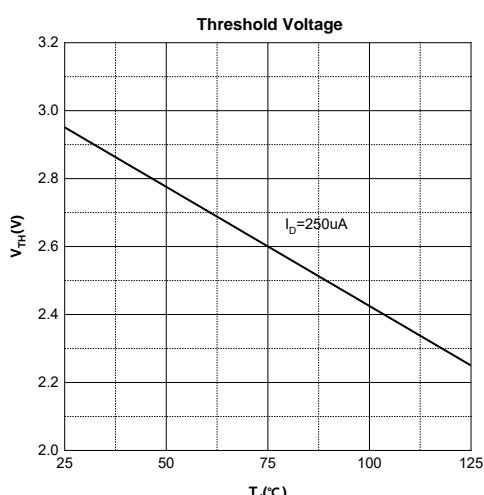
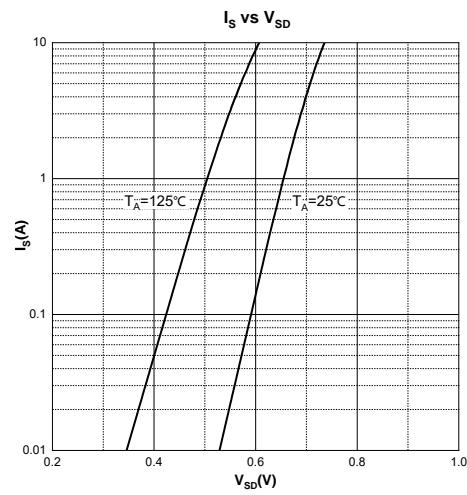
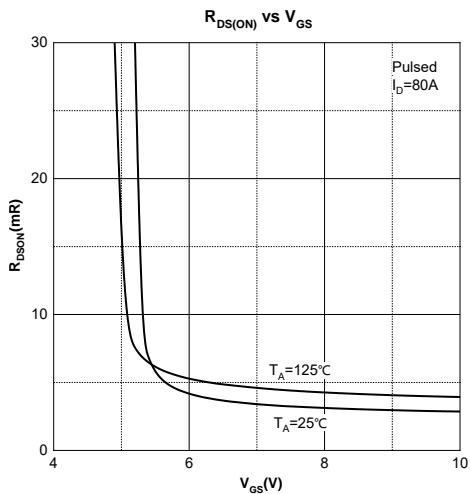
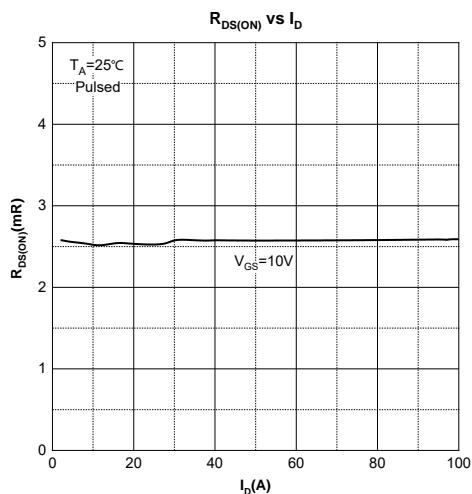
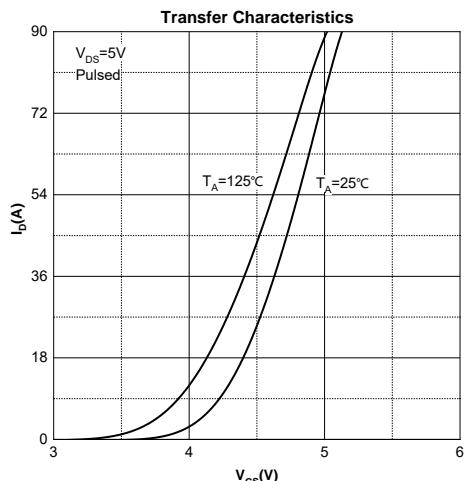
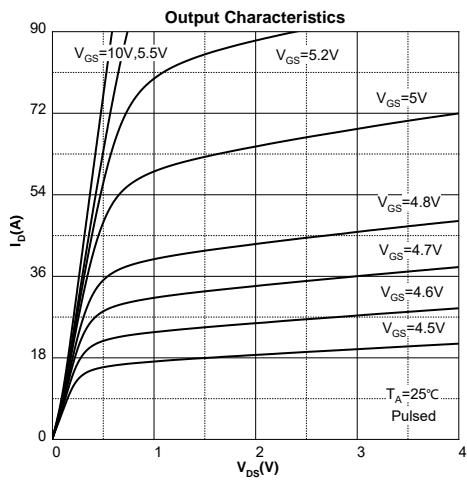
Parameter	Symbol	Value	Unit
Drain - Source Voltage	V_{DS}	100	V
Gate - Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹	I_D	257	A
	I_D	167	A
Pulsed Drain Current ²	I_{DM}	1028	A
Single Pulsed Avalanche Current ³	I_{AS}	70	A
Single Pulsed Avalanche Energy ³	E_{AS}	1225	mJ
Power Dissipation ⁵	P_D	379	W
Thermal Resistance from Junction to Ambient ⁶	$R_{\theta JA}$	59	$^\circ\text{C}/\text{W}$
Thermal Resistance from Junction to Case	$R_{\theta JC}$	0.33	$^\circ\text{C}/\text{W}$
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55~+150	$^\circ\text{C}$

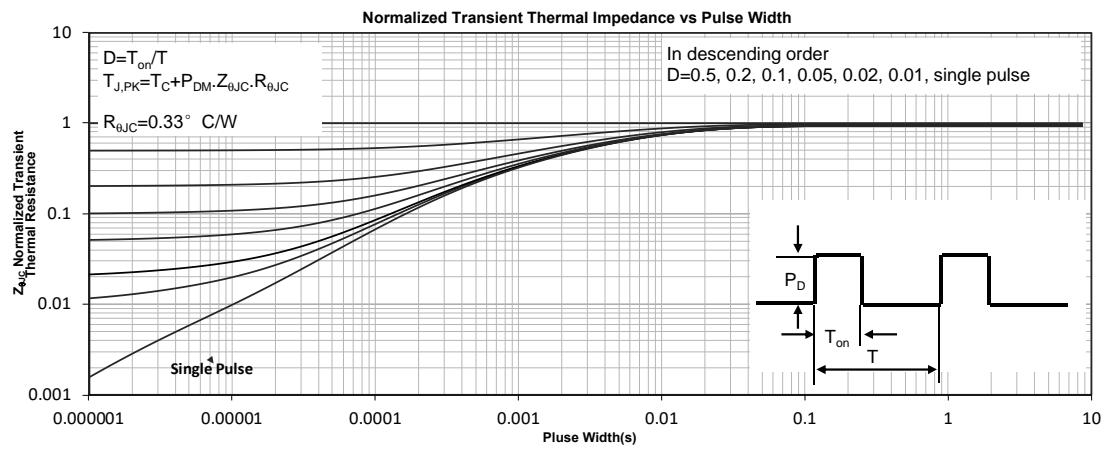
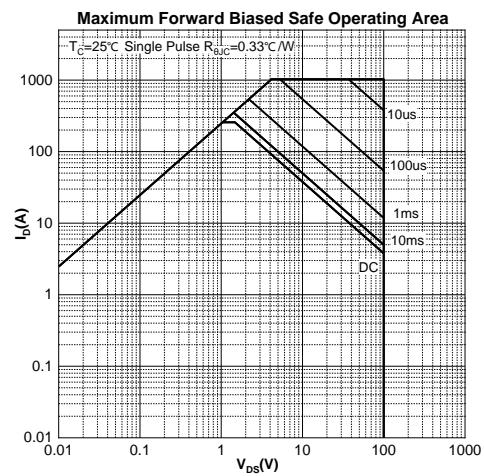
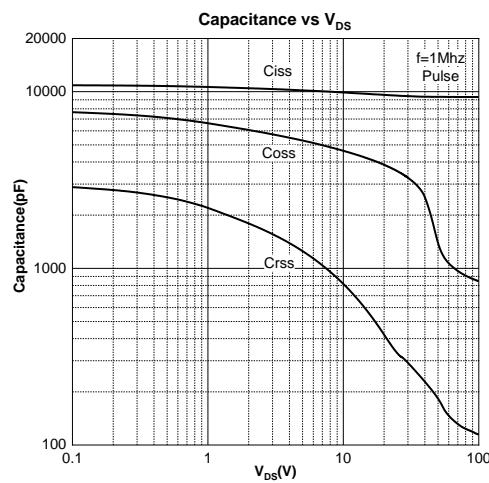
MOSFET ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

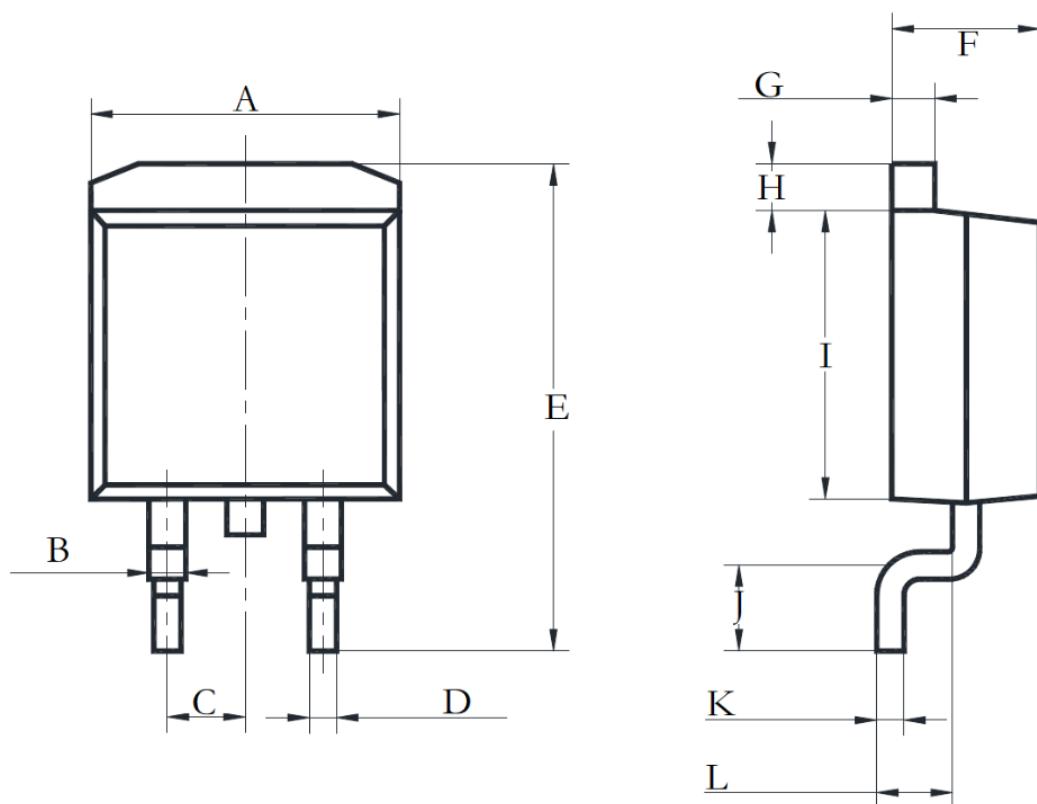
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Off Characteristics						
Drain - Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	100			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 100\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
Gate - Body Leakage Current	I_{GSS}	$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$			± 100	nA
On Characteristics⁴						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	2	3	4	V
Drain-source On-resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$		2.7	3.5	$\text{m}\Omega$
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{\text{DS}} = 50\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		9344		pF
Output Capacitance	C_{oss}			1335		
Reverse Transfer Capacitance	C_{rss}			190		
Gate Resistance	R_g	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		2.4		Ω
Switching Characteristics						
Total Gate Charge	Q_g	$V_{\text{DS}} = 50\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}$		154		nC
Gate-source Charge	Q_{gs}			38		
Gate-drain Charge	Q_{gd}			45		
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 50\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 20\text{A}, R_G = 3\Omega$		28		ns
Turn-on Rise Time	t_r			79		
Turn-off Delay Time	$t_{\text{d}(\text{off})}$			110		
Turn-off Fall Time	t_f			86		
Source - Drain Diode Characteristics						
Diode Forward Voltage ⁴	V_{SD}	$V_{\text{GS}} = 0\text{V}, I_s = 20\text{A}$			1.2	V

Notes :

- 1.The maximum current rating is limited by package.And device mounted on a large heatsink
- 2.Pulse Test : Pulse Width $\leq 10\mu\text{s}$, duty cycle $\leq 1\%$.
- 3.E_{AS} condition: $V_{\text{DD}} = 50\text{V}, V_{\text{GS}} = 10\text{V}, L = 0.5\text{mH}, R_G = 25\Omega$ Starting $T_J = 25^\circ\text{C}$.
- 4.Pulse Test : Pulse Width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
- 5.The power dissipation P_D is limited by $T_{J(\text{MAX})} = 150^\circ\text{C}$.And device mounted on a large heatsink
- 6.Device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$.

Typical Characteristics




TO-263-2L Package Information


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	9.600	10.500	0.378	0.413
B	1.000	1.400	0.039	0.055
C	2.540REF		0.100REF	
D	0.680	0.940	0.027	0.037
E	14.600	15.880	0.575	0.625
F	4.400	4.800	0.173	0.189
G	1.140	1.400	0.045	0.055
H	1.140	1.400	0.045	0.055
I	8.250	9.650	0.325	0.380
J	2.290	2.790	0.090	0.110
K	0.360	0.650	0.014	0.026
L	2.030	2.790	0.080	0.110